



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

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Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada
Pedatadepalli, **TADEPALLIGUDEM – 534 101**, W.G. Dist, (A.P.)

Department of Electrical and Electronics Engineering

Faculty Publications Academic Year: 2018-19

S. No.	Name of the faculty	Title of paper	Name of the Journal	Volume No.,page No& year	SCI/SCOPUS /UGC/others	impact factor	peer reviewed
1	DR.CH.RAMBAB, N SRIHARIISH	USING BESS CONTROL OF REDUCED RATING DYNAMIC VOLTAGE RESTORER BY USING FUZZY LOGIC	International journal of innovative engineering and management research	7,73-81&June 2018	others	7.0112	yes
2	G Madhu sagar babu, V Rama narayana	A Comparison of Seven-Level Inverter Topologies with Minimum Number of Switches for Induction Motor Drive	International Journal of Engineering Science and Computing	8,17332-17336 & April 2018	others	5.611	yes
3	V Rama narayana	TCLC-STATCOM a long large compensation range and less DC-Link voltage for loads using ANN	International journal of professional engineering studies	10,177-184&June 2018	others	4.825	yes